

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JAE-BON KOO

Serial No.: 10/764,525

Examiner: KIM, SUN M.

Filed: 27 January 2004

Art Unit: 2813

For: FLAT PANEL DISPLAY WITH ANODE ELECTRODE LAYER AS  
POWER SUPPLY LAYER AND FABRICATION METHOD THEREOF

**AMENDMENT AFTER FINAL**

**Mail Stop: AF**

Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Final Office action mailed on 24 November 2009 (Paper No. 20091120A), entry of the following amendments and remarks, re-examination and reconsideration are respectfully requested.

**Please enter.**

/SMK/ 01/26/2010

Folio: P56937

Date: 1/25/10

I.D.: REB/ML/st